

ABSTRACT

A control loop circuit is disclosed for optimizing a power supply output under varying load conditions. The power supplye has a main loop amplifier and an output stage to generate the output. The control loop circuit includes a static control path
5 coupled to the output and having an error amplifier. The error amplifier is operative to generate an error signal for presentation to the main loop amplifier where the error signal represents the difference between a desired output and a sensed output. A dynamic control path is coupled to the error amplifier output and is responsive to the error signal to generate a dynamic compensation signal. The dynamic control path
10 has an output coupled to the main loop amplifier output.

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100